Cervoz Industrial SSD

2.5” SATA
Momentum Series (MLC)
M305 Family

Product Datasheet
## Revision History

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Description</th>
</tr>
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<tr>
<td>2016.05.06</td>
<td>1.0</td>
<td>First Released</td>
</tr>
<tr>
<td>2016.08.02</td>
<td>2.0</td>
<td>Wide Temperature Solution Information Added</td>
</tr>
<tr>
<td>2017.01.23</td>
<td>2.1</td>
<td>16GB Capacity Added</td>
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<tr>
<td>2017.06.20</td>
<td>2.2</td>
<td>1.4 Ordering Information Added</td>
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<td></td>
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<td>2.2 64GB Performance Updated</td>
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<td>2017.10.11</td>
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1. Product Overview

1.1 Introduction
Cervoz Industrial 2.5” SATA SSD M305 family is a high capacity Solid State Disk product that is in compliance with the 2.5” form factor and SATA III standards. The device design is based on the 7pin for data segment and 15pin for power segment standards.

M305 family uses superior quality preselected multi-level cell (MLC) NAND flash memory from the industry leading manufacturer Micron. This product includes both standard temperature range and wide temperature range options with various capacities to choose from.

M305 family offers outstanding performance and reliability; the product family is a perfect solution for price sensitive semi-industrial and general industrial applications.

1.2 Application Fields
- Industrial PC and Thin Client
- Ticketing, Examining and Testing Machine
- Military, Health and Automatic Equipment
- Server, Cloud Computer

1.3 Features
- Compliant with SATA III 6.0Gb/s (backward compatible to 1.5Gb/s and 3Gb/s)
- MLC NAND flash memory
- Capacity: 16GB ~ 256GB
- Operating as boot disk
- Low power consumption
- Shock resistance and anti-vibration
- Static and dynamic wear leveling
- Bad block management
- S.M.A.R.T. & TRIM command
- Fully compatible with Windows XP, Windows 7, Windows 8, Windows 10, Mac OS, and Linux
- RoHS compliant
1.4 Product Appearance & Models
Cervoz Industrial 2.5” SATA SSD M305

<table>
<thead>
<tr>
<th>M305 Family</th>
<th>Standard Temp. (0°C ~ 70°C) Model No.</th>
<th>M305 Family</th>
<th>Wide Temp. (-40°C ~ 85°C) Model No.</th>
<th>Capacity</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>CIS-2SM305MJB016GS</td>
<td>CIS-2SM305MKB032GS</td>
<td>N/A</td>
<td>16GB</td>
</tr>
<tr>
<td></td>
<td>CIS-2SM305MKB032GS</td>
<td>CIS-2SM305MKB032GS</td>
<td>N/A</td>
<td>32GB</td>
</tr>
<tr>
<td></td>
<td>CIS-2SM305MJB032GS</td>
<td>CIS-2SM305MKB064GS</td>
<td>N/A</td>
<td>64GB</td>
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<td></td>
<td>CIS-2SM305MLB064GS</td>
<td>CIS-2SM305MKB128GS</td>
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<td>128GB</td>
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<td></td>
<td>CIS-2SM305MLB256GS</td>
<td>CIS-2SM305MKB256GS</td>
<td>N/A</td>
<td>256GB</td>
</tr>
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</table>

Please Note:
Since certain storage capacity has to be reserved for firmware and controller management purposes; the physical capacity of the SATA flash module will be approximately 92.5% of the indicated capacity. If you need to install an image that has the exact (or close to) the indicated size of the flash module, please choose your flash module with a greater capacity.
## 2. Product Specifications

### 2.1 General Specifications

<table>
<thead>
<tr>
<th>Specification</th>
<th>Details</th>
</tr>
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<tbody>
<tr>
<td><strong>Form Factor</strong></td>
<td>2.5 inch</td>
</tr>
<tr>
<td><strong>Interface</strong></td>
<td>SATA III 6.0Gb/s (backward compatible to 3.0Gb/s, 1.5Gb/s)</td>
</tr>
<tr>
<td><strong>Connector</strong></td>
<td>SATA (7 + 15 pin)</td>
</tr>
<tr>
<td><strong>NAND Flash Type</strong></td>
<td>MLC</td>
</tr>
</tbody>
</table>
| **Capacity**           | Standard Temp.: 16GB/32GB/64GB/128GB/256GB  
                         | Wide Temp.: 32GB/64GB/128GB/256GB |
| **Sequential Read**    | up to 500 MB/s |
| **Sequential Write**   | up to 280 MB/s |
| **ECC Scheme**         | up to 66 bits / 1K Byte |
| **MTBF**               | 2,000,000 hours |
| **Terabyte Written (TBW)** | 16GB : 15.6  
                         | 32GB : 31  
                         | 64GB : 63  
                         | 128GB : 125  
                         | 256GB : 250 |
| **Low Power Management** | DIPM / HIPM mode |
| **Supply Voltage**     | +5V DC ± 5% |
| **Power Consumption**  | Active mode: < 1420mW  
                         | Idle mode: < 110mW |
| **Dimension (L x W x H)** | 100.00*69.85*7.00mm |
| **Weight**             | 55 ± 1g |
2.2 Performance

The performance was measured with below PC configuration:

- Platform: GIGABYTE GA-Z97MX-Gaming5 (Intel Z97)
- RAM: Cervoz CIR-S3DUSI1302G (DDR3 2G 1333MHz)
- Operation Systems: Windows 7 SP1
- Testing Utility: Crystal Disk Mark v3.0 x64
- SATAIII port (6.0 Gb/s) performance

<table>
<thead>
<tr>
<th>Capacity</th>
<th>16GB</th>
<th>32GB</th>
<th>64GB</th>
<th>128GB</th>
<th>256GB</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sequential Read (max.)</td>
<td>130MB/s</td>
<td>255MB/s</td>
<td>500MB/s</td>
<td>500MB/s</td>
<td>500MB/s</td>
</tr>
<tr>
<td>Sequential Write (max.)</td>
<td>20MB/s</td>
<td>40MB/s</td>
<td>80MB/s</td>
<td>160MB/s</td>
<td>280MB/s</td>
</tr>
<tr>
<td>4KB Random Read (QD32)</td>
<td>23MB/s</td>
<td>45MB/s</td>
<td>85MB/s</td>
<td>110MB/s</td>
<td>120MB/s</td>
</tr>
<tr>
<td>4KB Random Write (QD32)</td>
<td>19MB/s</td>
<td>40MB/s</td>
<td>80MB/s</td>
<td>150MB/s</td>
<td>220MB/s</td>
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</table>

Actual performance may vary depending on use conditions and environment

*Performance is based on different NAND Flash IC.

2.3 Drive Capacity

<table>
<thead>
<tr>
<th>Capacity</th>
<th>Cylinders</th>
<th>Heads</th>
<th>Sectors</th>
<th>Max LBA</th>
</tr>
</thead>
<tbody>
<tr>
<td>16GB</td>
<td>16,383</td>
<td>16</td>
<td>63</td>
<td>31,277,232</td>
</tr>
<tr>
<td>32GB</td>
<td>16,383</td>
<td>16</td>
<td>63</td>
<td>62,533,296</td>
</tr>
<tr>
<td>64GB</td>
<td>16,383</td>
<td>16</td>
<td>63</td>
<td>125,045,424</td>
</tr>
<tr>
<td>128GB</td>
<td>16,383</td>
<td>16</td>
<td>63</td>
<td>250,069,680</td>
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<tr>
<td>256GB</td>
<td>16,383</td>
<td>16</td>
<td>63</td>
<td>500,118,192</td>
</tr>
</tbody>
</table>
2.4  Electronic Specifications
2.4.1  Block Diagram
### 2.4.2 Pin Assignment

#### Signal Segment

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>S1</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>S2</td>
<td>Rx+</td>
<td></td>
</tr>
<tr>
<td>S3</td>
<td>Rx-</td>
<td>Differential Signals Pair Receive</td>
</tr>
<tr>
<td>S4</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>S5</td>
<td>Tx-</td>
<td>Differential Signals Pair Transmit</td>
</tr>
<tr>
<td>S6</td>
<td>Tx+</td>
<td></td>
</tr>
<tr>
<td>S7</td>
<td>GND</td>
<td>Ground</td>
</tr>
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</table>

#### Key & Spacing

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Function</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>P1</td>
<td>NC</td>
<td>NA</td>
</tr>
<tr>
<td>P2</td>
<td>NC</td>
<td>NA</td>
</tr>
<tr>
<td>P3</td>
<td>NC</td>
<td>NA</td>
</tr>
<tr>
<td>P4</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>P5</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>P6</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>P7</td>
<td>V5/PC</td>
<td>+5V Power supply, Pre-charge</td>
</tr>
<tr>
<td>P8</td>
<td>V5</td>
<td>+5V Power supply</td>
</tr>
<tr>
<td>P9</td>
<td>V5</td>
<td>+5V Power supply</td>
</tr>
<tr>
<td>P10</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>P11</td>
<td>NC</td>
<td>NA</td>
</tr>
<tr>
<td>P12</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>P13</td>
<td>NC</td>
<td>NA</td>
</tr>
<tr>
<td>P14</td>
<td>NC</td>
<td>NA</td>
</tr>
<tr>
<td>P15</td>
<td>NC</td>
<td>NA</td>
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2.5 Environmental Specifications

<table>
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<tr>
<th>Type</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Temperature</td>
<td>Standard Temperature Operating: 0°C~70°C</td>
</tr>
<tr>
<td></td>
<td>Standard Temperature Storage: -40°C~85°C</td>
</tr>
<tr>
<td></td>
<td>Wide Temperature Operating: -40°C~85°C</td>
</tr>
<tr>
<td></td>
<td>Wide Temperature Storage: -50°C~95°C</td>
</tr>
<tr>
<td>Humidity</td>
<td>Operating &amp; Storage: 5~95%, Non-Condensing</td>
</tr>
<tr>
<td>Vibration</td>
<td>Non-Operating: 20G, 10Hz~2000Hz</td>
</tr>
<tr>
<td>Shock</td>
<td>Non-Operating: 1500G, 0.5ms</td>
</tr>
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2.6 Mechanical Specifications

<table>
<thead>
<tr>
<th>Type</th>
<th>Value</th>
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</thead>
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<tr>
<td>Form Factor</td>
<td>2.5” SATA</td>
</tr>
<tr>
<td>Length</td>
<td>100.00 mm +0.2 / -0.15 mm</td>
</tr>
<tr>
<td>Width</td>
<td>69.85 mm +0.2 / -0.15 mm</td>
</tr>
<tr>
<td>Thickness</td>
<td>6.90 mm ± 0.2 mm</td>
</tr>
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### 3. Supported Command

#### 3.1 List of Command Sets

<table>
<thead>
<tr>
<th>Command</th>
<th>Code</th>
<th>Protocol</th>
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<tbody>
<tr>
<td><strong>General Feature Set</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Execute Device Diagnostic</td>
<td>90h</td>
<td>Execute device diagnostic</td>
</tr>
<tr>
<td>Flush Cache</td>
<td>E7h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Identify Device</td>
<td>ECh</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>Initialize Drive Parameters</td>
<td>91h</td>
<td>Non-data</td>
</tr>
<tr>
<td>NOP</td>
<td>00h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Read Buffer</td>
<td>E4h</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>Read DMA</td>
<td>C8h</td>
<td>DMA</td>
</tr>
<tr>
<td>Read Log Ext</td>
<td>2Fh</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>Read Multiple</td>
<td>C4h</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>Read Sector(s)</td>
<td>20h</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>Read Verify Sector(s)</td>
<td>40h or 41h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Set Feature</td>
<td>EFh</td>
<td>Non-data</td>
</tr>
<tr>
<td>Set Multiple Mode</td>
<td>C6h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Write Buffer</td>
<td>E8h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>Write DMA</td>
<td>CAh</td>
<td>DMA</td>
</tr>
<tr>
<td>Write Multiple</td>
<td>C5h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>Write Sector(s)</td>
<td>30h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td><strong>Power Management Feature Set</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Check Power Mode</td>
<td>E5h or 98h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Idle</td>
<td>E3h or 97h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Idle Immediate</td>
<td>E1h or 95h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Sleep</td>
<td>E6h or 99h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Standby</td>
<td>E2h or 96h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Standby Immediate</td>
<td>E0h or 94h</td>
<td>Non-data</td>
</tr>
<tr>
<td><strong>Security Mode Feature Set</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Security Set Password</td>
<td>F1h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>Security Unlock</td>
<td>F2h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>Security Erase Prepare</td>
<td>F3h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Security Erase Unit</td>
<td>F4h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>Security Freeze Lock</td>
<td>F5h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Security Disable Password</td>
<td>F6h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>Command</td>
<td>Code</td>
<td>Protocol</td>
</tr>
<tr>
<td>---------------------------------------</td>
<td>------</td>
<td>--------------</td>
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<td>SMART Feature Set</td>
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<tr>
<td>SMART Disable Operations</td>
<td>B0h</td>
<td>Non-data</td>
</tr>
<tr>
<td>SMART Enable/Disable Autosave</td>
<td>B0h</td>
<td>Non-data</td>
</tr>
<tr>
<td>SMART Enable Operations</td>
<td>B0h</td>
<td>Non-data</td>
</tr>
<tr>
<td>SMART Execute OFF-LINE Immediate</td>
<td>B0h</td>
<td>Non-data</td>
</tr>
<tr>
<td>SMART Read Log</td>
<td>B0h</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>SMART Read Data</td>
<td>B0h</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>SMART Read Threshold</td>
<td>B0h</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>SMART Return Status</td>
<td>B0h</td>
<td>Non-data</td>
</tr>
<tr>
<td>SMART Save Attribute Values</td>
<td>B0h</td>
<td>Non-data</td>
</tr>
<tr>
<td>SMART Write Log</td>
<td>B0h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>Host Protected Area Feature Set</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Read Native Max Address</td>
<td>F8h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Set Max Address</td>
<td>F9h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Set Max Set Password</td>
<td>F9h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>Set Max Lock</td>
<td>F9h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Set Max Freeze Lock</td>
<td>F9h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Set Max Unlock</td>
<td>F9h</td>
<td>PIO data-out</td>
</tr>
<tr>
<td>48-bit Address Feature Set</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Flush Cache Ext</td>
<td>EAh</td>
<td>Non-data</td>
</tr>
<tr>
<td>Read Sector(s) Ext</td>
<td>24h</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>Read DMA Ext</td>
<td>25h</td>
<td>DMA</td>
</tr>
<tr>
<td>Read Multiple Ext</td>
<td>29h</td>
<td>PIO data-in</td>
</tr>
<tr>
<td>Read Native Max Address Ext</td>
<td>27h</td>
<td>Non-data</td>
</tr>
<tr>
<td>Read Verify Sector(s) Ext</td>
<td>42h</td>
<td>Non-data</td>
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<tr>
<td>Set Max Address Ext</td>
<td>37h</td>
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<td>Write DMA Ext</td>
<td>35h</td>
<td>DMA</td>
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<td>Write Multiple Ext</td>
<td>39h</td>
<td>PIO data-out</td>
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<td>Write Sector(s) Ext</td>
<td>34h</td>
<td>PIO data-out</td>
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<tr>
<td>NCQ Feature Set</td>
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<td></td>
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<tr>
<td>Read FPDMA Queued</td>
<td>60h</td>
<td>DMA Queued</td>
</tr>
<tr>
<td>Write FPDMA Queued</td>
<td>61h</td>
<td>DMA Queued</td>
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<tr>
<td>Miscellaneous and Historical Commands</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Data Set Management</td>
<td>06h</td>
<td>DMA</td>
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<tr>
<td>Seek</td>
<td>70h</td>
<td>Non-data</td>
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### 3.2 Identify Device

<table>
<thead>
<tr>
<th>Word</th>
<th>F/V</th>
<th>Default Value</th>
<th>Description</th>
</tr>
</thead>
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<tr>
<td>0</td>
<td>F</td>
<td>0040h</td>
<td>General configuration</td>
</tr>
<tr>
<td>1</td>
<td>X</td>
<td>XXXXh</td>
<td>Default number of cylinders</td>
</tr>
<tr>
<td>2</td>
<td>V</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>00XXh</td>
<td>Default number of heads</td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>0000h</td>
<td>Obsolete</td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>0240h</td>
<td>Obsolete</td>
</tr>
<tr>
<td>6</td>
<td>F</td>
<td>XXXXh</td>
<td>Default number of sectors per track</td>
</tr>
<tr>
<td>7 - 8</td>
<td>V</td>
<td>XXXXh</td>
<td>Number of sectors per card (Word 7 = MSW, Word 8 = LSW)</td>
</tr>
<tr>
<td>9</td>
<td>X</td>
<td>0000h</td>
<td>Obsolete</td>
</tr>
<tr>
<td>10 - 19</td>
<td>F</td>
<td>XXXXh</td>
<td>Serial number in ASCII (Right justified)</td>
</tr>
<tr>
<td>20</td>
<td>X</td>
<td>0002h</td>
<td>Obsolete</td>
</tr>
<tr>
<td>21</td>
<td>X</td>
<td>0002h</td>
<td>Obsolete</td>
</tr>
<tr>
<td>22</td>
<td>X</td>
<td>0000h</td>
<td>Obsolete</td>
</tr>
<tr>
<td>23 - 26</td>
<td>F</td>
<td>XXXXh</td>
<td>Firmware revision in ASCII Big Endian Byte Order in Word</td>
</tr>
<tr>
<td>27 - 46</td>
<td>F</td>
<td>XXXXh</td>
<td>Model number in ASCII (Left justified) Big Endian Byte Order in Word</td>
</tr>
<tr>
<td>47</td>
<td>F</td>
<td>8001h</td>
<td>Maximum number of sectors on Read/Write Multiple command</td>
</tr>
<tr>
<td>48</td>
<td>F</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>49</td>
<td>F</td>
<td>0300h</td>
<td>Capabilities</td>
</tr>
<tr>
<td>50</td>
<td>F</td>
<td>0400h</td>
<td>Capabilities</td>
</tr>
<tr>
<td>51</td>
<td>F</td>
<td>0200h</td>
<td>PIO data transfer cycle timing mode</td>
</tr>
<tr>
<td>52</td>
<td>X</td>
<td>0000h</td>
<td>Obsolete</td>
</tr>
<tr>
<td>53</td>
<td>F</td>
<td>0007h</td>
<td>Field validity</td>
</tr>
<tr>
<td>54</td>
<td>X</td>
<td>XXXXh</td>
<td>Current numbers of cylinders</td>
</tr>
<tr>
<td>55</td>
<td>X</td>
<td>XXXXh</td>
<td>Current numbers of heads</td>
</tr>
<tr>
<td>56</td>
<td>X</td>
<td>XXXXh</td>
<td>Current sectors per track</td>
</tr>
<tr>
<td>57 - 58</td>
<td>X</td>
<td>XXXXh</td>
<td>Current capacity in sectors (LBAs) (Word 57 = LSW, Word 58 = MSW)</td>
</tr>
<tr>
<td>59</td>
<td>F</td>
<td>0101h</td>
<td>Multiple sector setting</td>
</tr>
<tr>
<td>60 - 61</td>
<td>F</td>
<td>XXXXh</td>
<td>Total number of user addressable logical sectors for 28-bit commands (DWord)</td>
</tr>
<tr>
<td>Word</td>
<td>F/V</td>
<td>Default Value</td>
<td>Description</td>
</tr>
<tr>
<td>------</td>
<td>-----</td>
<td>---------------</td>
<td>-------------</td>
</tr>
<tr>
<td>62</td>
<td>X</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>63</td>
<td>F</td>
<td>0207h</td>
<td>Multiword DMA transfer supports MDMA mode 0, 1 and 2</td>
</tr>
<tr>
<td>64</td>
<td>F</td>
<td>0003h</td>
<td>Advanced PIO modes supported</td>
</tr>
<tr>
<td>65</td>
<td>F</td>
<td>0078h</td>
<td>Minimum Multiword DMA transfer cycle time per word</td>
</tr>
<tr>
<td>66</td>
<td>F</td>
<td>0078h</td>
<td>Recommended Multiword DMA transfer cycle time</td>
</tr>
<tr>
<td>67</td>
<td>F</td>
<td>0078h</td>
<td>Minimum PIO transfer cycle time without flow control</td>
</tr>
<tr>
<td>68</td>
<td>F</td>
<td>0078h</td>
<td>Minimum PIO transfer cycle time with IORDY flow control</td>
</tr>
<tr>
<td>69</td>
<td>F</td>
<td>4000h</td>
<td>Additional supported</td>
</tr>
<tr>
<td>70</td>
<td>F</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>71</td>
<td>F</td>
<td>001Fh</td>
<td>Queue depth</td>
</tr>
<tr>
<td>76</td>
<td>F</td>
<td>030Eh</td>
<td>Serial ATA capabilities</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Supports Serial ATA Gen3</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Supports Serial ATA Gen2</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Supports Serial ATA Gen1</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Supports Phy event counters log</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Supports receipt of host initiated power management requests</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Supports Native Command Queuing</td>
</tr>
<tr>
<td>77</td>
<td>F</td>
<td>0080h</td>
<td>Serial ATA additional capability</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• DevSleep to Reduced Power State</td>
</tr>
<tr>
<td>78</td>
<td>F</td>
<td>0148h</td>
<td>Serial ATA features supported</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Supports Device Sleep</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Supports software settings preservation</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>• Device supports initiating power management</td>
</tr>
<tr>
<td>79</td>
<td>V</td>
<td>0040h</td>
<td>Reserved</td>
</tr>
<tr>
<td>80</td>
<td>F</td>
<td>03FCCh</td>
<td>Major version number (ACS-2)</td>
</tr>
<tr>
<td>81</td>
<td>F</td>
<td>0000h</td>
<td>Minor version number</td>
</tr>
<tr>
<td>82</td>
<td>F</td>
<td>702Bh</td>
<td>Command sets supported 0</td>
</tr>
<tr>
<td>83</td>
<td>F</td>
<td>7500h</td>
<td>Command sets supported 1</td>
</tr>
<tr>
<td>84</td>
<td>F</td>
<td>4002h</td>
<td>Command sets supported 2</td>
</tr>
<tr>
<td>85</td>
<td>V</td>
<td>XXXXh</td>
<td>Command set/feature enabled</td>
</tr>
<tr>
<td>88</td>
<td>V</td>
<td>007Fh</td>
<td>Ultra DMA mode supported and selected</td>
</tr>
<tr>
<td>89</td>
<td>F</td>
<td>0003h</td>
<td>Time required for a Normal Erase mode Security Erase Unit</td>
</tr>
<tr>
<td>90</td>
<td>F</td>
<td>0001h</td>
<td>Time required for an Enhanced Erase mode Security Erase Unit</td>
</tr>
<tr>
<td>91</td>
<td>V</td>
<td>0000h</td>
<td>Current advanced power management value</td>
</tr>
<tr>
<td>92</td>
<td>V</td>
<td>FFFFh</td>
<td>Master password identifier</td>
</tr>
<tr>
<td>93</td>
<td>V</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>100</td>
<td>V</td>
<td>XXXXh</td>
<td>Maximum user LBA for 48-bit address feature set</td>
</tr>
<tr>
<td>Word</td>
<td>F/V</td>
<td>Default Value</td>
<td>Description</td>
</tr>
<tr>
<td>--------</td>
<td>-----</td>
<td>---------------</td>
<td>------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>104</td>
<td>V</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>105</td>
<td>F</td>
<td>0100h</td>
<td>Maximum number of 512-byte blocks per Data Set Management</td>
</tr>
<tr>
<td>106 - 127</td>
<td>V</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>128</td>
<td>V</td>
<td>0009h</td>
<td>Security status</td>
</tr>
<tr>
<td>129 - 159</td>
<td>X</td>
<td>XXXXh</td>
<td>Vendor specific</td>
</tr>
<tr>
<td>160</td>
<td>F</td>
<td>0000h</td>
<td>Power requirement description</td>
</tr>
<tr>
<td>161</td>
<td>X</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>162</td>
<td>F</td>
<td>0000h</td>
<td>Key management schemes supported</td>
</tr>
<tr>
<td>163</td>
<td>F</td>
<td>0000h</td>
<td>CF Advanced True IDE Timing mode capability and setting</td>
</tr>
<tr>
<td>164 - 168</td>
<td>V</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>169</td>
<td>F</td>
<td>0001h</td>
<td>Data Set Management supported</td>
</tr>
<tr>
<td>170 - 216</td>
<td>V</td>
<td>XXXXh</td>
<td>Reserved</td>
</tr>
<tr>
<td>217</td>
<td>F</td>
<td>0001h</td>
<td>Non-rotating media (SSD)</td>
</tr>
<tr>
<td>218 - 221</td>
<td>X</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>222</td>
<td>F</td>
<td>107Fh</td>
<td>Transport major revision (SATA Rev 3.1)</td>
</tr>
<tr>
<td>223 - 254</td>
<td>X</td>
<td>0000h</td>
<td>Reserved</td>
</tr>
<tr>
<td>255</td>
<td>X</td>
<td>XXXXh</td>
<td>Integrity word</td>
</tr>
</tbody>
</table>

Notes:
1. F = content (byte) is fixed and does not change.
2. V = content (byte) is variable and may change depending on the state of the device or the commands executed by the device.
3. X = content (byte) is vendor specific and may be fixed or variable.
### 3.3 Identify Device SMART Feature Set

<table>
<thead>
<tr>
<th>Value</th>
<th>Command</th>
<th>Value</th>
<th>Command</th>
</tr>
</thead>
<tbody>
<tr>
<td>D0h</td>
<td>Read Data</td>
<td>D5h</td>
<td>Read Log</td>
</tr>
<tr>
<td>D1h</td>
<td>Read Attribute Threshold</td>
<td>D6h</td>
<td>Write Log</td>
</tr>
<tr>
<td>D2h</td>
<td>Enable/Disable Autosave</td>
<td>D8h</td>
<td>Enable SMART Operations</td>
</tr>
<tr>
<td>D3h</td>
<td>Save Attribute Values</td>
<td>D9h</td>
<td>Disable SMART Operations</td>
</tr>
<tr>
<td>D4h</td>
<td>Execute OFF-LINE Immediate</td>
<td>DAh</td>
<td>Return Status</td>
</tr>
</tbody>
</table>

If the reserved size is below the threshold, the status can be read from the Cylinder Register using the Return Status command (DAh).

### 3.4 SMART Data Feature

The following 512 bytes make up the device SMART data structure. Users can obtain the data using the “Read Data” command (D0h).

<table>
<thead>
<tr>
<th>Byte</th>
<th>F / V</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 - 1</td>
<td>X</td>
<td>Revision code</td>
</tr>
<tr>
<td>2 - 361</td>
<td>X</td>
<td>Vendor specific (see 4.3.2)</td>
</tr>
<tr>
<td>362</td>
<td>V</td>
<td>Off-line data collection status</td>
</tr>
<tr>
<td>363</td>
<td>X</td>
<td>Self-test execution status byte</td>
</tr>
<tr>
<td>364 - 365</td>
<td>V</td>
<td>Total time in seconds to complete off-line data collection activity</td>
</tr>
<tr>
<td>366</td>
<td>X</td>
<td>Vendor specific</td>
</tr>
<tr>
<td>367</td>
<td>F</td>
<td>Off-line data collection capability</td>
</tr>
<tr>
<td>368 - 369</td>
<td>F</td>
<td>SMART capability</td>
</tr>
<tr>
<td>370</td>
<td>F</td>
<td>Error logging capability</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• 7-1  Reserved</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• 0 1  Device error logging supported</td>
</tr>
<tr>
<td>371</td>
<td>X</td>
<td>Vendor specific</td>
</tr>
<tr>
<td>372</td>
<td>F</td>
<td>Short self-test routine recommended polling time (in minutes)</td>
</tr>
<tr>
<td>373</td>
<td>F</td>
<td>Extended self-test routine recommended polling time (in minutes)</td>
</tr>
<tr>
<td>374</td>
<td>F</td>
<td>Conveyance self-test routine recommended polling time (in minutes)</td>
</tr>
<tr>
<td>375 - 385</td>
<td>R</td>
<td>Reserved</td>
</tr>
<tr>
<td>386 - 395</td>
<td>F</td>
<td>Firmware version/date code</td>
</tr>
<tr>
<td>396 - 399</td>
<td>F</td>
<td>Reserved</td>
</tr>
<tr>
<td>400 - 408</td>
<td>F</td>
<td>‘SM2246XT’</td>
</tr>
<tr>
<td>409 - 415</td>
<td>X</td>
<td>Vendor specific</td>
</tr>
<tr>
<td>416 - 417</td>
<td>F</td>
<td>Reserved</td>
</tr>
<tr>
<td>418 - 419</td>
<td>V</td>
<td>Number of spare block</td>
</tr>
<tr>
<td>420</td>
<td>F</td>
<td>Reserved</td>
</tr>
<tr>
<td>421 - 422</td>
<td>V</td>
<td>Average erase count</td>
</tr>
<tr>
<td>423</td>
<td>F</td>
<td>Reserved</td>
</tr>
<tr>
<td>Byte</td>
<td>F / V</td>
<td>Description</td>
</tr>
<tr>
<td>----------</td>
<td>-------</td>
<td>---------------------------</td>
</tr>
<tr>
<td>424 - 425</td>
<td>V</td>
<td>Max erase count</td>
</tr>
<tr>
<td>426</td>
<td>F</td>
<td>Reserved</td>
</tr>
<tr>
<td>427 - 428</td>
<td>V</td>
<td>Min erase count</td>
</tr>
<tr>
<td>429 - 510</td>
<td>X</td>
<td>Vendor specific</td>
</tr>
<tr>
<td>511</td>
<td>V</td>
<td>Data structure checksum</td>
</tr>
</tbody>
</table>

**Note:**
1. F = content (byte) is fixed and does not change.
2. V = content (byte) is variable and may change depending on the state of the device or the commands executed by the device.
3. X = content (byte) is vendor specific and may be fixed or variable.
4. R = content (byte) is reserved and shall be zero.
### 3.5 SMART Attributes

The following table defines the vendor specific data in byte 2 to 361 of the 512-byte SMART data.

<table>
<thead>
<tr>
<th>Attribute ID (hex)</th>
<th>Raw Attribute Value</th>
<th>Attribute Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>MSB 00 00 00 00 00 00</td>
<td>Read error rate</td>
</tr>
<tr>
<td>05</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Reallocated sectors count</td>
</tr>
<tr>
<td>09</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Power-on hours</td>
</tr>
<tr>
<td>0C</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Power cycle count</td>
</tr>
<tr>
<td>A0</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Uncorrectable sector count when read/write</td>
</tr>
<tr>
<td>A1</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Number of valid spare block</td>
</tr>
<tr>
<td>A2</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Number of cache data block</td>
</tr>
<tr>
<td>A3</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Number of initial invalid block</td>
</tr>
<tr>
<td>A4</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Total erase count</td>
</tr>
<tr>
<td>A5</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Maximum erase count</td>
</tr>
<tr>
<td>A6</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Minimum erase count</td>
</tr>
<tr>
<td>A7</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Average erase count</td>
</tr>
<tr>
<td>C0</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Power-off retract count</td>
</tr>
<tr>
<td>C2</td>
<td>MSB 00 00 00 00 00 00</td>
<td>Controlled temperature</td>
</tr>
<tr>
<td>C3</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Hardware ECC recovered</td>
</tr>
<tr>
<td>C4</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Reallocation event count</td>
</tr>
<tr>
<td>C7</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Ultra DMA CRC error count</td>
</tr>
<tr>
<td>F1</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Total LBAs written (each write unit = 32MB)</td>
</tr>
<tr>
<td>F2</td>
<td>LSB MSB 00 00 00 00 00</td>
<td>Total LBAs read (each read unit = 32MB)</td>
</tr>
</tbody>
</table>
### 4. Part No. Decoder

#### 4.1 Part No. Decoder

<table>
<thead>
<tr>
<th></th>
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<th></th>
<th></th>
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<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>XXX</td>
<td>-</td>
<td>XX</td>
<td>X</td>
<td>XXX</td>
<td>X</td>
<td>X</td>
<td>X</td>
<td>XXX</td>
<td>X</td>
</tr>
</tbody>
</table>

1. **Product Line**
- CIS: Cervoz Industrial SSD
- CIM: Cervoz Industrial Memory Card
- CIE: Cervoz Industrial Embedded Module

2. **Form Factor**
- 2S: 2.5” SATA
- 2P: 2.5” PATA
- CF: CompactFlash
- CA: CFast
- MS: mSATA
- HM: Half Size mSATA
- HS: Half Slim
- M4: M.2 2242
- M6: M.2 2260
- M8: M.2 2280
- 0V: PATA Disk 40pin Vertical
- 4V: PATA Disk 44pin Vertical
- 4L: PATA Disk 44pin Horizontal Left
- 7T: SATA Disk 7pin Vertical Tall
- 7L: SATA Disk 7pin Horizontal Left
- 7R: SATA Disk 7pin Horizontal Right

3. **Product Series**
- S: Supreme Series (SLC)
- R: Reliance Series (RO-MLC)
- M: Momentum Series (MLC)

4. **Cervoz Family Code**
Bus and Internal Control for Cervoz Product Families

5. **Flash Brand**
- M: Micron
- T: Toshiba

6. **Flash Capacity**
- A: 256Mb
- B: 512Mb
- C: 1Gb
- D: 2Gb
- E: 4Gb
- F: 8Gb
## 7. Flash Mode
Internal Control for Flash Mode

## 8. Module Capacity

<table>
<thead>
<tr>
<th>Module Capacity</th>
<th>Capacity</th>
</tr>
</thead>
<tbody>
<tr>
<td>128M</td>
<td>128MB</td>
</tr>
<tr>
<td>256M</td>
<td>256MB</td>
</tr>
<tr>
<td>512M</td>
<td>512MB</td>
</tr>
<tr>
<td>001G</td>
<td>1GB</td>
</tr>
<tr>
<td>002G</td>
<td>2GB</td>
</tr>
<tr>
<td>004G</td>
<td>4GB</td>
</tr>
<tr>
<td>008G</td>
<td>8GB</td>
</tr>
<tr>
<td>016G</td>
<td>16GB</td>
</tr>
<tr>
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<tr>
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<td>64GB</td>
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<td>128G</td>
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<tr>
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<td>256GB</td>
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<tr>
<td>512G</td>
<td>512GB</td>
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## 9. Operating Temperature

<table>
<thead>
<tr>
<th>Operating Temperature</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>S</td>
<td>Standard Grade (0~ +70°C)</td>
</tr>
<tr>
<td>W</td>
<td>Wide Temperature Grade (-40 ~ +85°C)</td>
</tr>
</tbody>
</table>